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Analog.5780

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 6,797,591

Issued: 09/28/2004

Name of Patentee: Gormley et al.

Title of Invention: METHOD FOR FORMING A SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR DEVICE FORMED BY THE METHOD

Attention Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR PTO MISTAKE (37 CFR 1.322(a))

1. Attached, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.
2. The exact page and line number where the errors are shown correctly in the application file are:

Page 1, line 12
Page 6, line 13
Page 7, line 21
Page 11, line 29
Page 13, line 20
Page 14, lines 23, 27 & 28
Page 15, line 19 & 20
Page 18, line 18
Page 19, line 9, 20
Page 21, line 3
Claim 38 as amended in an amendment mailed 1/24/03

Certificate
OCT 05 2006
of Correction

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450

Date: 9-27-06

Elizabeth M. Ball

OCT 10 2006

3. Please send the Certificate to:

Name: Matthew E. Connors

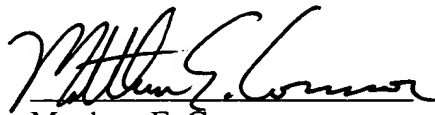
Address: Gauthier & Connors, LLP

225 Franklin Street, Suite 2300

Boston, Massachusetts 02110

Reg. No.:33,298

SIGNATURE OF ATTORNEY

A handwritten signature in black ink, appearing to read "Matthew E. Connors", written over a horizontal line.

Matthew E. Connors
Gauthier & Connors, LLP
225 Franklin Street, Suite 2300
Boston, MA 02110
Tel. No. (617) 426-9180
Extension: 112

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,797,591
DATED : 09/28/04
INVENTOR(S) : Gormley et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 1, line 18, delete "similarity" and replace with "similarly"

In Column 4, line 10, delete "note" and replace with "not"

In Column 4, line 55, insert the word "layer" after the word "stop"

In Column 7, line 20 delete "layer 4 to 400 microns. The lower layer 4 acts as a handle" and replace with "layer 4 is 400 microns. The lower layer 4 acts as a handle"

In Column 8, line 27 delete "functions" and replace with "function"

In Column 9, line 8, delete "35" and replace with "36"

In Column 9, line 15, delete "etched"

In Column 9, line 16, insert the word "etched" after the word "been"

In Column 9, line 45, delete "from the which the micro-mechanical components are to be" and insert, "from which the micro-mechanical components are to be"

In Column 9, line 46, delete "formed may be of any other suitable material, it will of" and replace with "formed may be of any other suitable material. It will of"

MAILING ADDRESS OF SENDER:

Gauthier & Connors LLP
225 Franklin Street Suite 2300
Boston, MA 02110

PATENT NO. 6,797,591

No. of additional copies

⇒ 1 of 2

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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In Column 10, claim 10, line 64, delete "second stop layer being bonded to the one of the second and" and replace with " second etch stop layer being bonded to the one of the second and"

In Column 11, claim 17, line 18, delete "16" and replace with "15"

In Column 11, claim 20, line 30, delete " second and third layers and of semiconductor material." and replace with " second and third layers are of semiconductor material."

In Column 12, claim 31, line 23, delete the word "each" and insert the word "etch"

In Column 12, claim 38, line 48 , delete "first layer is at least half the area of the component in plan" and insert "first layer adjacent the component is at least half the area of the component in plan"

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